Docket No.: 0038-0482PUS1

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 1, line 6 as follows:

With speeding up-faster ICs, a variation of electric current working therethrough must be

great, and a source voltage is varied by high-speed switching so that they are apt to be badly

influenced by noises. Further, an electric power consumption of ICs and driving voltage thereof

are reduced, so the driving voltage is highly varied by a slight variation of the source voltage;

namely ICs are apt to be badly influenced by a voltage variation. The above described problems

disturb speeding up-faster ICs, thus a decoupling capacitor is provided between a source line and

an earth line of a semiconductor element to solve the problems.

Please amend the header on page 2:

DISCLOSURE SUMMARY OF THE INVENTION

Please amend the paragraph beginning on page 7, line 10 as follows:

Electroconductive sections 22 are formed in the core plate 20, lands 18a are formed in the

cable layer 13, and connection terminals 40 are respectively connected to the lands 18a. The

semiconductor element 10 is electrically connected to the connection terminals 40 via the vias

15, the electroconductive sections 22, etc.[[-]]

JMS/DAB/ta

2